



UNIVERSITY OF RUHUNA

Faculty of Engineering

End-Semester 1 Examination in Engineering: December 2023

Module Number: EE1301

Module Name: Fundamentals of Electricity

[Three Hours]

[Answer all questions, each question carries 12.5 marks]

- Q1 a) i) What is a dependent or controlled source in an electric circuit?
 ii) What is the difference between super-node and super-mesh with regard to DC circuit analysis?
 iii) Find the nodal voltages of the DC circuit given in Figure Q1(a)-1 using **Nodal Analysis**.
 iv) Obtain **only** the required simultaneous equations using **Mesh Analysis** considering the DC circuit given in Figure Q1(a)-2.

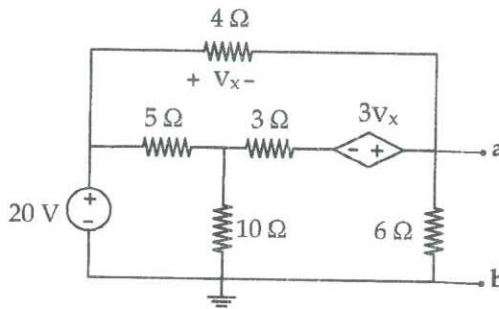


Figure Q1(a)-1

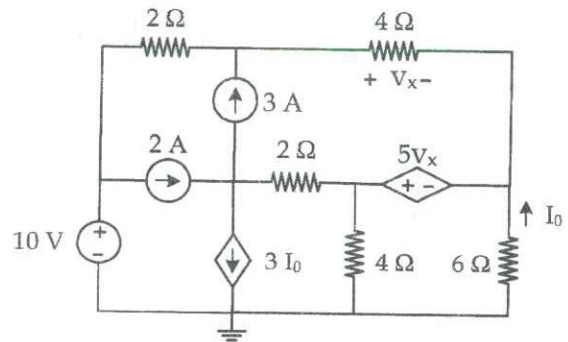


Figure Q1(a)-2

[6.5 Marks]

- b) i) For any electric circuit, if an entire circuit is replaced by its Thevenin equivalent circuit except the load, as shown in Figure Q1(b), show that the maximum power transferred to the load (P_{max}) is given by $P_{max} = \frac{V_{TH}^2}{4R_{TH}}$.

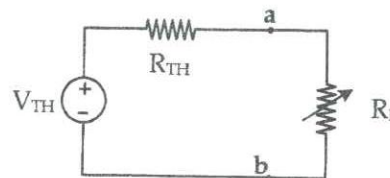


Figure Q1(b)

Note that all the notations have their usual meanings.

- ii) Find the Thevenin equivalent circuit across the terminals **a** and **b** in the DC circuit given in Figure Q1(a)-1.
 iii) Hence, calculate the maximum power that can be delivered to a load resistor connected across terminals **a** and **b** in the circuit shown in Figure Q1(a)-1.

[6.0 Marks]

- Q2 a) A sinusoid is a signal that has the form of the sine or cosine. A single-phase AC voltage source is given by $v(t) = 24 \sin(120t + 30^\circ)$ V. The sin function is used to derive the phase angle of the sinusoid.

Determine the following parameters of $v(t)$.

- | | |
|--------------------|-----------------|
| i) Amplitude | v) Peak factor |
| ii) RMS value | vi) Phase angle |
| iii) Average value | vii) Period |
| iv) Form factor | viii) Frequency |

[2.0 Marks]

- b) An inductor consists of a coil of conducting wire. The current through the inductor with the inductance of L is given by $i_L(t) = I_m \sin \omega t$.

- Briefly explain the behavior of an inductor considering basic electromagnetic principles.
- Obtain an expression for the instantaneous inductor voltage ($v_L(t)$).
- From the first principles, obtain an expression for the inductive reactance (X_L).
- If this inductor is connected in series with a resistor with the resistance of R and a single-phase voltage source with the RMS voltage of V , sketch the phasor diagram of the electric circuit considering **all phasors**.

[4.0 Marks]

- c) Consider the AC circuit given in Figure Q2(c). The RMS voltage of the 50 Hz AC source is 60 V.

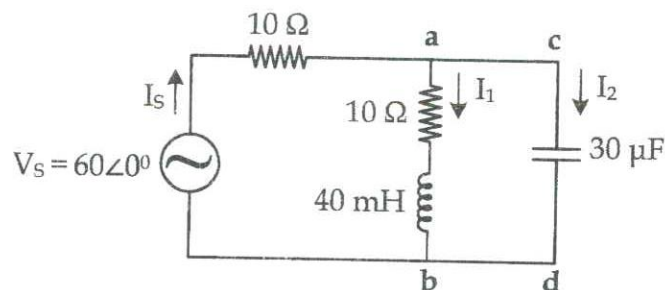


Figure Q2(c)

- Find the impedances connected across terminals **a** and **b** (Z_{ab}) and **c** and **d** (Z_{cd}), separately.
- Calculate the source current (I_s).
- Find the currents I_1 and I_2 through impedances Z_{ab} and Z_{cd} , respectively.
- Calculate the voltage across the terminals **a** and **b** (V_{ab}) and **c** and **d** (V_{cd}), separately.
- Draw the phasor diagram of the electric system given in Figure Q(c) including I_s , I_1 , I_2 , V_s , V_{ab} , V_{cd} phasors.

[6.5 Marks]

Q3 a) The instantaneous power $p(t)$ absorbed by an element is the product of the instantaneous voltage $v(t)$ and the instantaneous current $i(t)$ across the element. Here, $v(t) = V_m \cos(\omega t + \theta_v)$ and $i(t) = I_m \cos(\omega t + \theta_i)$.

i) Starting from instantaneous power $p(t)$, show that the average power dissipated at the element is,

$$P_{avg} = \frac{V_m I_m}{\sqrt{2} \sqrt{2}} \cos(\theta_v - \theta_i)$$

ii) If we consider that voltage phasor as the reference, express the Apparent power in terms of voltage and current phasors. Hence, obtain expressions for apparent power, complex power, active power, and reactive power.

iii) Draw the power triangle clearly indicating apparent power, active power, and reactive power.

iv) Define the power factor.

v) Using the results in Part ii) and Part iii), show that a capacitive load conventionally injects the reactive power.

[5.0 Marks]

b) A 10 KVA, 50 Hz load is rated at 0.8 power factor lagging. The load voltage must be kept at 480 V. The rated load is supplied by a 50 Hz single-phase voltage source by an electric circuit as illustrated in Figure Q3(b).

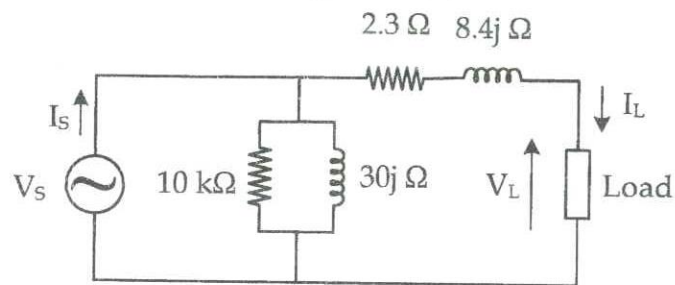


Figure Q3(b)

- Calculate the load current (I_L).
- Calculate the source voltage, source current and input power factor.
- Draw the phasor diagram indicating the phasors for load voltage, load current, source voltage and source current.
- Find the real, reactive and apparent power supplied by the source.
- Calculate the required parallel capacitance to improve the input power factor to 0.9 lagging.

[7.5 Marks]

Q4 a) Consider a positive (**abc**) sequence balanced star-connected (**Y**) three phase supply. If the instantaneous voltage of phase **a** is given by $v_{an}(t) = \sqrt{2}V_p \sin \omega t$.

- Write expressions for the instantaneous voltages of phase **b** and phase **c**.
- Show that $V_{ab} = \sqrt{3} V_p \angle 30^\circ$.

- iii) The line-to-line voltage of lines **a** and **b** of a positive sequence star-connected (Y) balanced three-phase supply is given as $v_{ab}(t) = 208\angle 60^\circ$ V. Draw the phasor diagram showing the line-to-line voltages and phase voltages of the three-phase supply.
- iv) A three-phase star-connected (Y) balanced load draws a line current of I_Y from a balanced three-phase source. What is the line current, if the same load is connected in delta (Δ) configuration and supplied by the same three-phase source.

[4.0 Marks]

- b) A three-phase star-connected (Y) balanced load is supplied by a positive sequence balanced three-phase supply as illustrated in Figure Q4(b). The per-phase load impedance consists of a resistor and an inductor connected in series. The resistance of resistor and the reactance of inductor are 25Ω and $32j \Omega$, respectively. The line-to-line voltage across terminals **a** and **b** is 400 V.

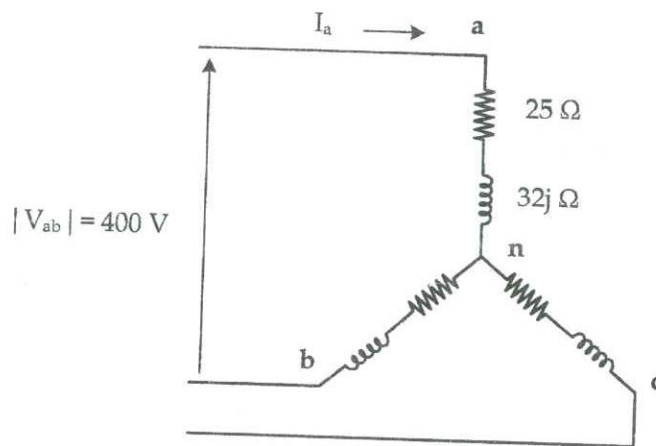


Figure Q4(b)

- Calculate the per-phase equivalent load impedance.
- Calculate the line current of line **a** (I_a).
- Sketch the three-phase phasor diagram showing all the line-to-line voltages, line currents and phase voltages.
- What is the power factor of the load.
- Calculate the total active power, reactive power and apparent power drawn by the three-phase load.
- If a capacitor bank is installed at the load end which can supply half of the reactive power requirement of the load, what is the improved power factor of the system?

[8.5 Marks]