

Interface Defect Layer Engineering for Realistic Modeling of 2D/3D Mixed Dimensional Perovskite Solar Cells

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Abstract—This study focuses on a comprehensive investigation into the influence of interface defects on the performance of 2D/3D mixed-dimensional perovskite solar cells (PSCs). To address the limitations of the conventional Interface Defect Density (IDD) model, the Interface Defect Layer (IDL) model has been introduced to more realistically represent the effect of the interface defects and recombination losses. Based on two defect models, a 2D/3D mixed dimensional PSC with a device architecture of FTO/TiO₂/MAPbI₃/BA₂MA₂Pb₃I₁₀/Spiro-OMeTAD/Au was simulated. 2D/3D and 3D/ETL interfaces were identified as critical interfaces in device performance degradation in using the IDD model-based simulations. Accordingly, thin IDL (IDL1 and IDL2) were placed at these interfaces, and their thickness and defect density were systematically optimized. A thickness of 1 nm and a defect density of 10¹³ cm⁻³ were identified to be the most effective values for suppressing recombination and improving efficiency. The study reveals that IDL1 (3D/ETL interface) has a significant effect on photovoltaic parameters than IDL2 (2D/3D interface). The IDL model device achieved a simulated PCE of 19.25%, V_{oc} of 0.98V J_{sc} of 24.68 mA/cm² and FF of 79.83% at the optimal thickness and defect density values.

Keywords - 2D/3D mixed dimensional PSCs, interface defects, interface defect layer, stability, SCAPS.

I. INTRODUCTION

Perovskite Solar Cells (PSCs) are leading third-generation photovoltaic technologies, offering higher Power Conversion Efficiency (PCE) along with cost-effective fabrication practices. However, they are suffering from long term operational stability issues upon exposure to high temperatures, humidity and UV radiation [1]. As a solution for that, 2D perovskite materials, proven to be highly stable, are incorporated with 3D perovskite to enhance the stability. The resultant 2D/3D mixed-dimensional perovskite solar cells serve as a promising approach to achieving both high efficiency and improved stability[2-4]. By integrating 2D perovskite layers as a capping layer on 3D perovskites, it facilitates suppressing ion migration, and passivates surface defects enhancing resistance to heat, moisture and UV radiation [4,5].

In conventional 2D/3D mixed dimensional PSCs are formed from several key layers, including the back contact electrode, Hole Transport Layer (HTL), 2D perovskite capping layer, 3D perovskite light harvesting layer, Electron Transport Layer (ETL), Front contact. In this device architecture, there are three pivotal interfaces: 2D/HTL, 2D/3D and 3D/ETL. In the carrier transportation process, photogenerated carriers in the absorber layer are transported through these interfaces to the corresponding electrodes. These interfaces can introduce localized defect concentrations due to structural



disorder, lattice mismatch, , and incomplete chemical bonding[6]. They serve as nonradiative recombination centers, disrupting charge flow and drastically reducing the overall device performance [7].

Conventional numerical approaches to solar cell simulation have a tendency to neglect the severe spatial non-uniformity of interfacial defects. Consequently, the impact of localized recombination at interfaces is usually not considered. Addressing this modelling limitation is necessary in order to facilitate accurate device simulation and correct accounting of the effects of the interface defect [8,9].

In this study, a systematic numerical investigation was conducted on a 2D/3D mixed-dimensional PSC employing SCAPS-1D simulation software. First, interface defect density (IDD) sensitivity analysis is performed to identify the most critical interface. Then, an Interface Defect Layer (IDL) approach [9-11] is adopted at the identified interface to match localized recombination zones.

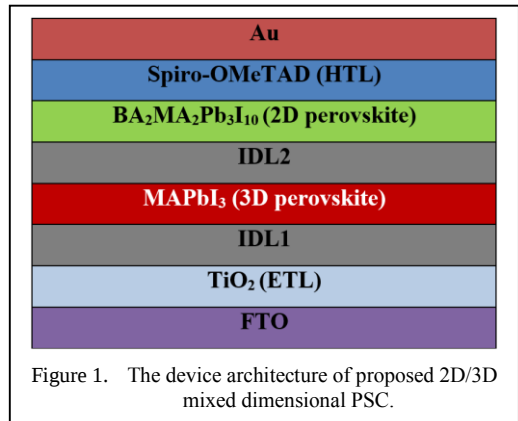
II. METHODOLOGY

A. Device Design

The device is simulated according to the PIN (Positive-Intrinsic-Negative) configuration. Titanium Dioxide (TiO_2) and Spiro-OMeTAD were chosen as ETL and HTL, respectively. These two materials are widely used in SCAPS 1D simulations as they provide well-defined parameters, ensuring the accuracy of solar cell modelling. As 3D perovskite, Methyl Ammonium Lead Iodide (MAPbI_3) was selected, and Butyl Ammonium Lead Iodide ($n=3$) [$\text{BA}_2\text{MA}_2\text{Pb}_3\text{I}_{10}$] was inserted as the capping layer. In this device design, Fluorine-doped Tin Dioxide (FTO) and Gold (Au) serve as the front and back electrode contacts, respectively. Fig. 1 illustrates the device architecture of the simulated PSC.

B. Device Simulation

All simulations were carried out using the drift-diffusion model under AM 1.5 Solar Spectrum of SCAPS 1D simulation software. The simulation parameters for the simulation were taken from previously published literature sources as mentioned in Table I. The electron thermal velocity and hole thermal velocity are taken as 10^7 cms^{-1} . The capture cross-section of the hole and electron is $1 \cdot 10^{-15} \text{ cm}^2$.



III. RESULTS AND DISCUSSION

A. Effect of Interface Defect Density (IDD) on Different Interfaces (The conventional approach)

In the 2D/3D mixed dimensional PSCs, there are three primary interfaces: 2D/HTL, 2D/3D, and 3D/ETL. The effect of interface defect densities of each interface has been investigated by varying the IDD at each interface. As shown in Fig. 2, a notable difference in the behavior of performance can be observed across the three interfaces. The photovoltaic parameters showed negligible response for the interface defect density variations in the 2D/HTL interface. In contrast, all photovoltaic parameters are highly sensitive to the 3D/ETL interface defect density variation, significantly degrading all the parameters when increasing the defect density. Meanwhile, 2D/3D interface defects show a moderate impact on device performance, resulting in a mild decline in PCE, V_{oc} and FF.

The reasons behind these variations can be explained through interface charge accumulation and recombination. The majority of charge carrier accumulation and recombination happens at the 3D/ETL interface. In addition to that, holes generated within the absorber layers have a higher possibility to successfully reach the HTL without trapping, whereas electrons are more susceptible to being trapped at the 3D/ETL interface [12]. In the 2D/3D interface, there is a balance between charge transportation and the defect-tolerance nature of the 2D perovskite material, which may tolerate severe nonradiative recombination. Hence, the 2D/3D interface exhibits a moderate impact compared to the highly recombination-prone 3D/ETL interface.

TABLE I. INPUT PARAMETERS USED FOR THE SCAPS SIMULATIONS [2,9,13-15].

Parameters	FTO	TiO ₂	IDL 1	CH ₃ NH ₃ PbI ₃	IDL2	BA ₂ MA ₂ Pb ₃ I ₁₀	Spiro-OMeTAD
Layer thickness (nm)	500	100	8	Varied	8	1000	30
Bandgap (eV)	3.5	3.26	1.41	1.55	1.41	1.6	2.9
Electron Affinity (eV)	4	4.2	4.17	3.9	4.17	3.87	2.2
Dielectric permittivity (relative)	9	38	8.2	3.2	8.2	5.9	3
Effective density of states of the conduction band C _B (1/cm ³)	2.2·10 ¹⁸	2.2·10 ¹⁷	1·10 ¹⁸	2.8·10 ²⁰	1·10 ¹⁸	7.24·10 ¹⁷	2.5·10 ¹⁸
Effective density of states of the valence band V _B (1/cm ³)	1.8·10 ¹⁹	1.8·10 ¹⁸	1·10 ¹⁸	3.9·10 ²⁰	1·10 ¹⁸	1.5·10 ¹⁸	1·10 ¹⁹
Electron mobility (cm ² /Vs)	20	2·14	1.6	11.8	1.6	1.4	2 ·10 ⁻⁴
Hole mobility (cm ² /Vs)	10	1·13	1.6	11.8	1.6	1.4	2 ·10 ⁻⁴
Shallow uniform donor density N _D (1/cm ³)	2·10 ¹⁹	6·10 ¹⁹	0	1·10 ⁹	0	0	0
Shallow uniform acceptor density N _A (1/cm ³)	1·10 ¹⁵	0	1·10 ¹²	1·10 ⁹	1·10 ¹²	0	1×10 ¹⁸
Defect density	1·10 ¹²	1·10 ¹²	1·10 ¹⁸	1·10 ¹²	1·10 ¹⁸	1·10 ¹⁵	1·10 ¹²

Overall, in 2D/3D mixed-dimensional perovskite solar cells, the 3D/ETL interface plays a vital role in charge transport relative to the 2D/HTL interface, making it particularly vulnerable to defect density variations.

B. Effect of Interface Defect Layer (IDL) at the Critical Interface

To make the simulation results more realistic, avoiding the limitations of the conventional interface defect density model, a thin IDL was inserted at the 2D/3D and ETL/3D interfaces, as

shown in Fig. 1. The IDL parameters were optimized in terms of both thickness and the defect density. This approach reflects physical degradation at the interface, as supported by previous findings reporting defect-rich zones spreading several nanometers long due to lattice mismatch and chemical instability [11]. Optimization facilitates assessing the device sensitivity to interfacial recombination and enables the identification of tolerable defect levels for efficient performance [8,9].

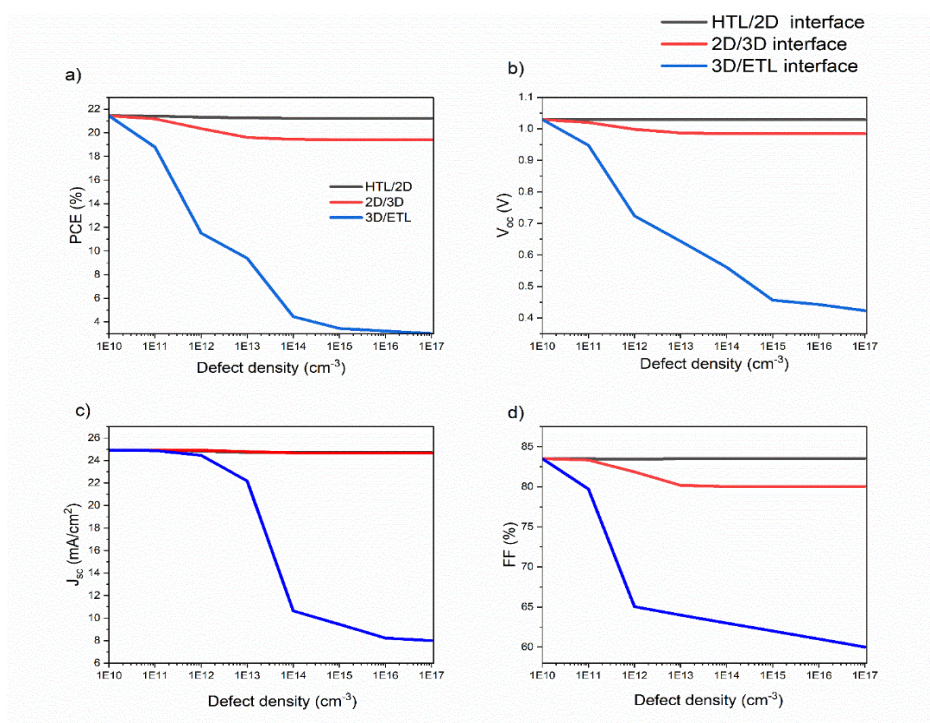


Figure 2. Effect of interface defect density on device performance (IDD model) a) PCE b) Voc c) Jsc and d) FF.

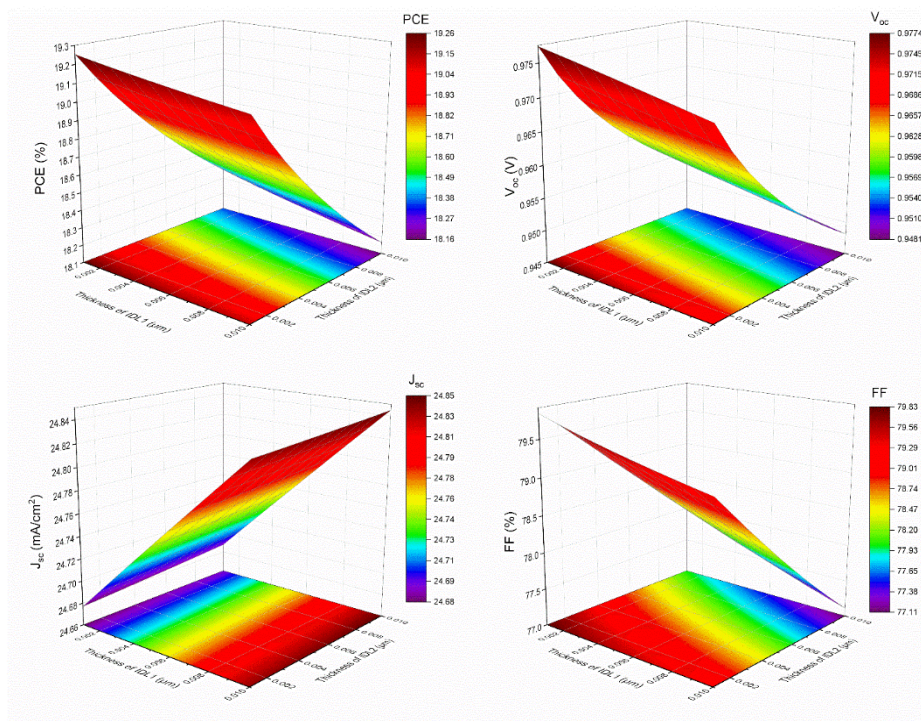


Figure 3. Effect of thickness of IDL on device performance a) PCE b) Voc c) Jsc and d) FF.

1) Effect of IDL thickness on the performance of the device

As interface defect acts as charge recombination centers, there is a strong correlation between the performance of PSCs and the interface defect layers IDL1 and IDL2 that are located between the ETL/3D and 2D/3D interfaces, respectively. Based on the given initial parameters, the thickness of IDL1 and IDL2 varied from 1 nm to 10 nm. The resulting performance trends are graphically represented in Fig. 3.

Fig. 3(a) showed the behavior of PCE according to thickness of IDL1 and IDL2. Regardless of the thickness of IDL1, there is no crucial impact on PCE, highlighting the dominant role of IDL2 in determining the performance. As the thickness of IDL2 increases, the PCE has declined from 19.25% to 18.35% at IDL1 thickness of 1 nm. This decline can be attributed to the improved recombination rate of electrons and holes, induced by increased thickness of the interfacial defect layer [9].

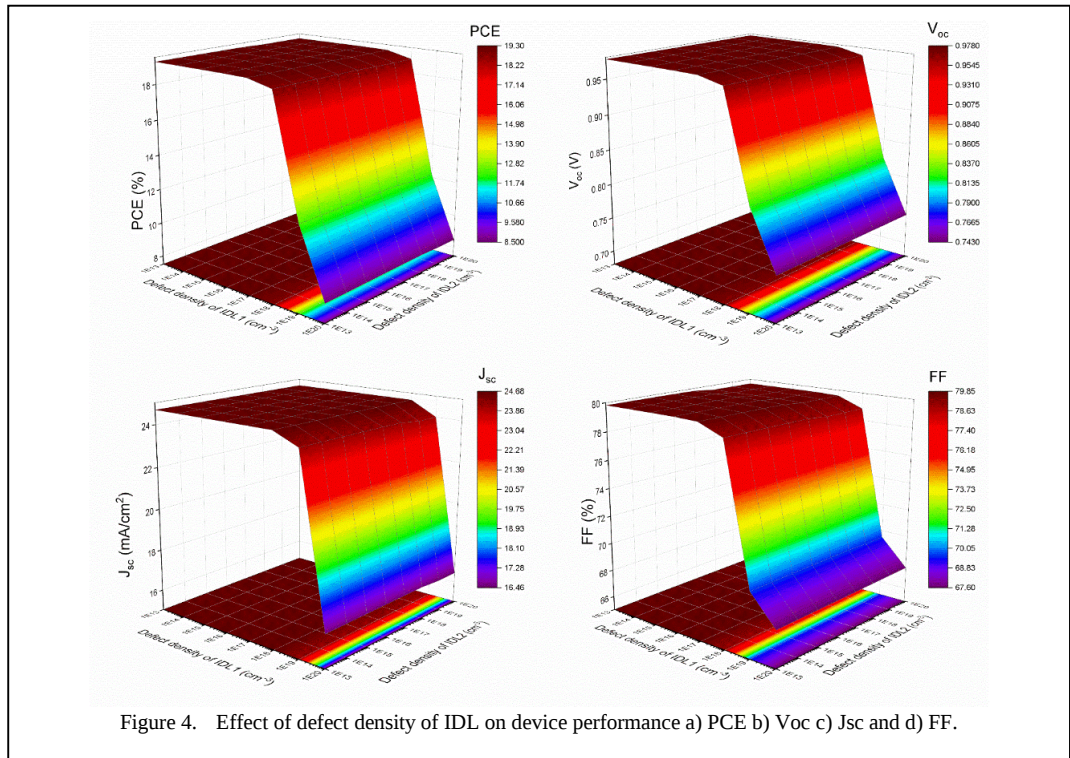
When the thickness of the interface defect layer IDL2 is held constant, the V_{oc} shows negligible change when increasing IDL1 thickness, as depicted in Fig. 3(b). However, V_{oc} slightly declines when IDL2 thickness rises,

indicating that IDL2 has the principal impact on V_{oc} .

As per Fig 3(c), J_{sc} remains unchanged when increasing the IDL2 thickness. However, J_{sc} slightly increases with IDL1 thickness enhancement. Fig. 3(d) exhibits the response of the fill factor (FF) to the thickness variations. FF declines with the thickness enhancement of both layers. However, compared to IDL1, IDL2 thickness variations have a significant effect on FF. By considering both variations, a 1 nm thickness of both layers has been selected as the optimum thickness value for the next steps of the simulation.

2) Effect of defect densities of the IDL on the performance of the device

Interfacial recombination has a substantial impact on the overall performance of 2D/3D mixed dimensional PSCs. Therefore, to represent realistic device behavior and appropriately address the challenge of quantifying the recombination rates in real devices [9], the impact of defect densities of IDL layers was systematically investigated. The defect densities of both layers varied between 10^{13} – 10^{20} cm^{-3} , keeping the other parameters constant.



The performance trend with varying thickness is plotted in Fig. 4. When the IDL1 defect density increases to 10^{17} cm^{-3} , the device performance remains stable for all IDL1 defect densities. After that, there is a substantial reduction in PCE. However, PCE shows negligible response to the IDL2 defect density variations. Similar behavior can also be seen in the V_{oc} , J_{sc} and FF variations as well highlighting the dominant influence of defect density of IDL1 on performance of the device. Therefore, the optimum defect density for both IDL1 and IDL2 was selected as 10^{13} cm^{-3} .

C. Comparison Between IDD and IDL Approaches

By taking the optimum thickness and defect densities of the IDLs, performance of the device is calculated. The IV curves correspond to the two defect models that are illustrated in Fig. 5 and the device performances are mentioned in Table II. The comparative analysis between the interface IDD and IDL approaches reveals considerable differences in photovoltaic performance. In the IDD case, defects are treated as idealized, localized interface traps, which may underestimate the extent of recombination occurring at interfaces of 2D/3D mixed dimensional PSCs.

In the IDL model, a defect region extended for a few nanometers was introduced, which is a better representation of interface degradation, including interdiffusion and structural disorder. This spatially resolved defect region increases the effective recombination volume, resulting in

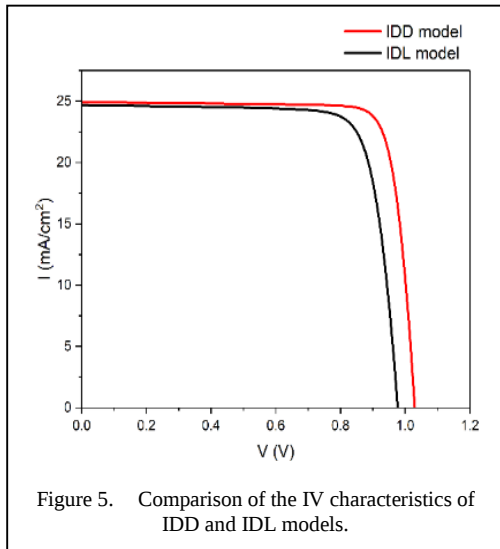


Figure 5. Comparison of the IV characteristics of IDD and IDL models.

a reduction of V_{oc} from 1.03 V to 0.98 V, and a decline in FF from 83.51% to 79.83%, highlighting greater resistance and recombination losses. The J_{sc} also slightly decreases from 24.92 to 24.68 mA/cm^2 . As a result, PCE drops from 21.43% to 19.25%.

In the conventional Interface Defect Density (IDD) model, the interface between two layers—such as the 3D perovskite and ETL—is treated as an infinitesimally thin plane that contains a fixed number of defect states per unit area, denoted by the interface trap density N_{it} .

The carrier recombination at this idealized interface is described by a boundary condition, expressed through the surface recombination velocity S , which quantifies how rapidly carriers recombine at the interface:

$$S = \sigma v_{th} N_{it}, \quad (1)$$

where σ is the carrier capture cross-section (cm^2) and v_{th} is the thermal velocity. The recombination current density is expressed as:

$$J_{rec} = qS \frac{(np - n_i^2)}{(n + p)}, \quad (2)$$

where S is surface recombination velocity, n, p electron and hole concentrations, n_i is the intrinsic carrier concentration, and q is the elementary charge.

This relation acts as a boundary condition linking carrier fluxes on either side of the interface:

$$\begin{aligned} J_n \big|_{x_0^-} J_p \big|_{x_0^+} &= qS(n - n_0) \\ J_p \big|_{x_0^-} J_n \big|_{x_0^+} &= qS(p - p_0) \end{aligned} \quad (3)$$

Thus, recombination is confined to a two-dimensional plane, and all defect effects are represented by the single parameter S .

Because the IDD model treats the interface as zero-thickness, it cannot capture spatially distributed defects, interdiffusion, or lattice disorder extending a few nanometers into adjacent layers. As a result, it often underestimates recombination losses and interface degradation effects. The proposed Interface Defect Layer (IDL) model, in contrast, introduces a finite layer of thickness of 1–10 nm

with distributed trap states of density. Within this region, recombination follows Shockley-Read-Hall (SRH) statistics, modifying the carrier continuity equations to include volumetric recombination terms. This allows spatially resolved treatment of recombination losses and electrostatic potential variations, providing a physically realistic depiction of interfacial degradation. Within the IDL, recombination is governed by the Shockley-Read-Hall (SRH) mechanism, and the recombination rate per unit volume is expressed as per Eqs. (4) and (5) [2].

$$R_{SRH} = \frac{np - n_i^2}{\tau_p(n + n_i) + \tau_n(p + p_i)}, \quad (4)$$

$$\tau_{n,p} = \frac{1}{\sigma v_{th,n,p} N_t}. \quad (5)$$

Here, R_{SRH} represents the rate of the Shockley-Read-Hall recombination, n and p are the concentrations of electrons and holes, respectively. τ_p, τ_n are the lifetimes of the electron and hole. v_{th}, n, p are the thermal velocities of electrons and holes, respectively. The carrier continuity equations inside the IDL region are modified to include the volumetric recombination term as shown in Eqs. (6) and (7).

$$\frac{dJ_n}{dx} = q(G - R_{SRH,IDL}), \quad (6)$$

$$\frac{dJ_p}{dx} = -q(G - R_{SRH,IDL}), \quad (7)$$

where G is the generation rate.

At the boundaries of the IDL, both carrier current densities and potentials are continuous as Eqs. (8) and (9):

$$J_n(ETL) = J_n(IDL) = J_n(3D), \quad (8)$$

$$\phi(ETL) = \phi(IDL) = \phi(3D), \quad (9)$$

where ϕ represents the electrostatic potential in the semiconductor.

This formulation treats the interface not as a discontinuity, but as a transition region where

TABLE II. COMPARISON OF THE PERFORMANCE OF DEFECT MODELS.

Device	PCE (%)	V_{oc} (V)	J_{sc} (mA/cm ²)	FF (%)
IDD	21.43	1.03	24.92	83.51
IDL	19.25	0.98	24.68	79.83

defects influence carrier transport and recombination in a spatially resolved manner. The quantitative comparison of these two models (Table II) shows a 2.18 % reduction in PCE, and a 0.05 V drop in V_{oc} in the IDL case, arising from the extended recombination region. Thus, the IDL model more accurately captures non-ideal interface behavior resulting from interdiffusion, lattice mismatch, and defect clustering.

IV. CONCLUSION

This study offers a comprehensive analysis of the impact of interface defects on the performance of 2D/3D mixed dimensional PSCs. To address the limitations associated with the conventional defect model. The Interface Defect Layer (IDL) model is introduced to represent the recombination process more realistically. The initial IDD model identified the 2D/3D and 3D/ETL interfaces are crucial for the device performance. Therefore, IDL layers were introduced in these two interfaces and the thickness and the defect density of the IDLs were optimized. A thickness of 1 nm and a defect density of 10^{13}cm^{-3} were determined to be optimum values, as they correspond to the highest device performance in terms of PCE, V_{oc} , and J_{sc} . The thickness and defect density of IDL1 have a more pronounced impact on the efficiency of the device compared to IDL2. The marked performance difference between the conventional IDD and the refined IDL models underscores the importance of advanced defect modeling in the design and simulation of 2D/3D mixed-dimensional PSCs.

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